TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of
Invention

Method and Apparatus for Creating Circuit Redundancy in Programmable Logic Devices

Application Number:

Date:

First Named Applicant:

Praveen K Samudrala

Confirmation Number:

Attorney Docket Number:

1372.136.PRC

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Documents being submitted:	Files
•	1070 100 DDO
us-request	1372_136_PRC-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	1372_136_PRC-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
application-body	Specification-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
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	isocyr1.ent
	isocyr2.ent
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	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	fig01.TIF
	fig02.TIF
	fig03.TIF
	fig04.TIF
	fig05.TIF
	fig06.TIF
Comments	